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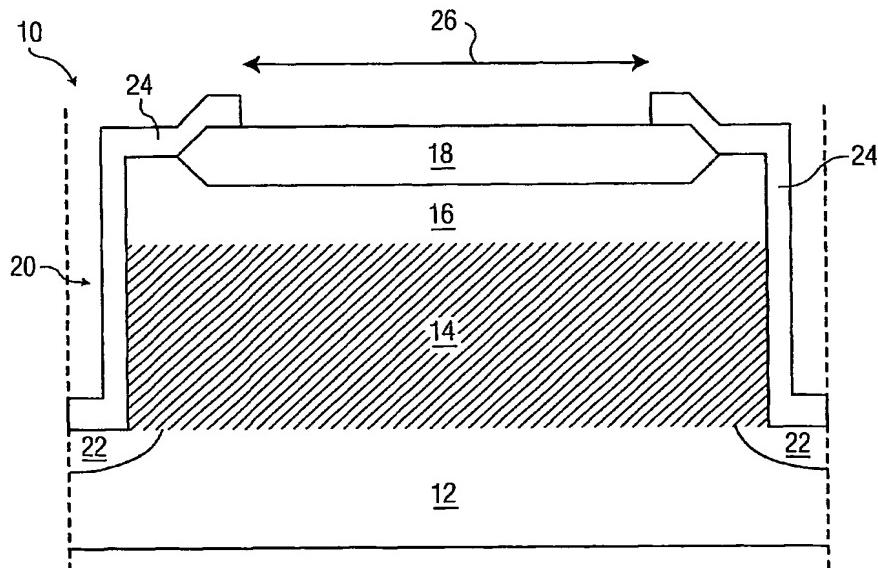
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(54) Title: SILICON-ON-INSULATOR PHOTODIODE OPTICAL MONITORING SYSTEM FOR COLOR TEMPERATURE CONTROL IN SOLID STATE LIGHT SYSTEMS



(57) Abstract: A silicon-on-insulator (SOI) photodiode optical monitoring method and system for color temperature control in solid state light systems. The method includes the steps of providing a plurality of SOI photodiodes, wherein each SOI photodiode includes a silicon substrate, a buried oxide layer formed on the silicon substrate, and a silicon layer formed on the buried oxide layer, and wherein the silicon layer of each SOI photodiode has a different thickness, determining a proportion of incident light passing through each SOI photodiode to the silicon substrate with respect to wavelength and the thickness of the silicon layer, and calculating color component intensities of the incident light based on the determined proportions.

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**Declarations under Rule 4.17:**

- *as to applicant's entitlement to apply for and be granted a patent (Rule 4.17(ii)) for the following designations AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NA, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RU, SC, SD, SE, SG, SK, SL, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, UZ, VC, VN, YU, ZA, ZM, ZW, ARIPO patent (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE,*
- *as to the applicant's entitlement to claim the priority of the earlier application (Rule 4.17(iii)) for all designations BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG)*

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